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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): G. MIYA, et al
Serial No.: (not yet assigned)
Filed: February 20, 2004
For: SEMICONDUCTOR PROCESSING APPARATUS AND A
DIAGNOSIS METHOD THEREFOR

REAFFIRMATION OF CLAIM FOR PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

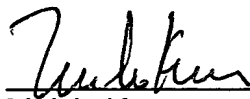
February 20, 2004

Sir:

Under the provisions of 35 USC §119 and 37 CFR §1.55, Applicants hereby claim the right of priority based on Japanese Patent Application No. 2002-130793, filed in Japan on May 2, 2002.

The certified copy of the above-referred to Japanese Patent Application was filed on October 31, 2002 in prior application Serial No. 10/229,072, filed August 28, 2002.

Respectfully submitted,



Melvin Kraus
Registration No. 22,466
ANTONELLI, TERRY, STOUT & KRAUS, LLP

MK/cee
(703) 312-6600